



BAT54JW

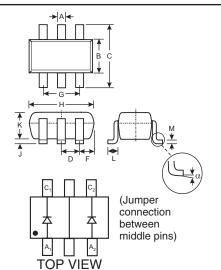
SURFACE MOUNT SCHOTTKY BARRIER DIODE ARRAY

Features

- Low Forward Voltage Drop
- Fast Switching
- Ultra-Small Surface Mount Package
- PN Junction Guard Ring for Transient and ESD Protection
- Lead Free/RoHS Compliant (Note 3)

Mechanical Data

- Case: SOT-363
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Solderable per MIL-STD-202, Method 208
- Lead Free Plating (Matte Tin Finish annealed over Alloy 42 leadframe).
- Orientation: See Diagram
- Weight: 0.006 grams (approx.)
- Marking: KLC (See Page 2)



SOT-363								
Dim	Min	Max						
Α	0.10	0.30						
В	1.15	1.35						
С	2.00	2.20						
D	0.65 Nominal							
F	0.30	0.40						
Н	1.80	2.20						
J	— 0.10							
K	0.90 1.00							
L	0.25	0.40						
M	0.10	0.25						
α	0°	8°						
All Dimensions in mm								

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Maximum Ratings @ T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit			
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage		V _{RRM} V _{RWM} V _R	30	V		
Forward Continuous Current (Note 1)	IF	200	mA			
Repetitive Peak Forward Current (Note	I _{FRM}	300	mA			
Forward Surge Current (Note 1)	Forward Surge Current (Note 1) @ t < 1.0s		600	mA		
Power Dissipation (Note 1)		P _d	200	mW		
Thermal Resistance, Junction to Ambient Air (Note 1)		$R_{ heta JA}$	625	°C/W		
Operating and Storage Temperature Rar	T _j , T _{STG}	-65 to +125	°C			

Electrical Characteristics @ T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 2)	V _{(BR)R}	30	_	_	V	$I_R = 100 \mu A$
Forward Voltage	VF	_	_	240 320 400 500 1000	mV	IF = 0.1mA IF = 1mA IF = 10mA IF = 30mA IF = 100mA
Reverse Leakage Current (Note 2)	I _R	_	_	2.0	μΑ	V _R = 25V
Total Capacitance	C _T	_	_	10	pF	V _R = 1.0V, f = 1.0MHz
Reverse Recovery Time	t _{rr}	_	_	5.0	ns	I_F = 10mA through I_R = 10mA to I_R = 1.0mA, R_L = 100 Ω

Notes:

- Device mounted on FR-4 PC board with recommended pad layout, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
- 2. Short duration test pulse used to minimize self-heating effect.
- 3. No purposefully added lead.



12

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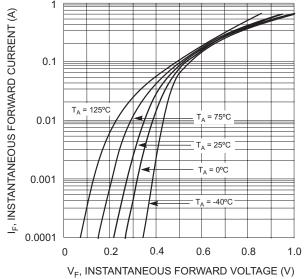
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0

0

C_T, TOTAL CAPACITANCE (pF)



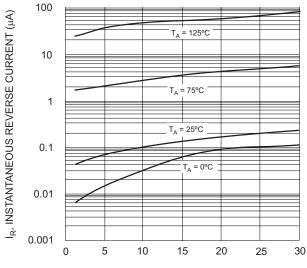
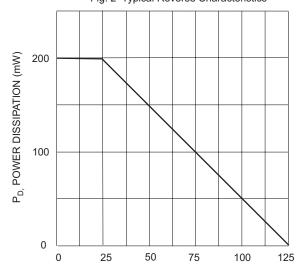


Fig. 1 Forward Characteristics



 V_R , INSTANTANEOUS REVERSE VOLTAGE (V) Fig. 2 Typical Reverse Characteristics



V_R, REVERSE VOLTAGE (V) Fig. 3 Typical Capacitance vs. Reverse Voltage

15

 T_A , AMBIENT TEMPERATURE (°C) Fig. 4 Power Derating Curve

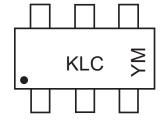
Ordering Information (Note 4)

5

Device	Packaging	Shipping		
BAT54JW-7-F	SOT-363	3000/Tape & Reel		

Notes: 4. For Packaging Details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



KLC = Product Type Marking Code YM = Date Code Marking Y = Year ex: N = 2002M = Month ex: 9 = September

Date Code Key

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012
Code	М	N	Р	R	S	Т	U	V	W	Х	Υ	Z
Month	Jan	Feb	March	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



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